

Substitute for form 1449/PTO THIRD SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/797,664
				Filing Date	March 11, 2004
				First Named Inventor	Pieter Van GROOS
				Art Unit	2851
				Examiner Name	KIM, Peter B.
Sheet	1	of	1	Attorney Docket Number	1857.7540002

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	US1				
	US2				
	US3				
	US4				
	US5				
	US6				
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
	FP1	JP 10-116760 A	05-06-1998	Nippon Kogaku KK		
	FP2					
	FP3					
	FP4					
	FP5					
	FP6					
	FP7					
	FP8					
	FP9					

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
	NPL1	Office Action for Chinese Application No. 2004100326869 issued April 4, 2008, 12 pgs.	
	NPL2	Peter van Zant, <i>Microchip Fabrication: A Practical Guide to Semiconductor Processing</i> , Third Edition, McGraw Hill Publishing Co., 1997 (the whole book).	
	NPL3		
	NPL4		
	NPL5		
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